

# PERFORMANCE SPECIFICATION

## RESISTOR NETWORKS, FIXED, FILM STYLE RZ080, 8 PIN SIP

This amendment forms a part of MIL-PRF-83401/8D, dated 18 March 1996, and is approved for use by all Departments and Agencies of the Department of Defense.

### PAGE 1

- \* Beneficial comments, delete and substitute:

“

Beneficial comments (recommendations, additions, deletions) and any pertinent data which may be of use in improving this document should be addressed to: Defense Supply Center, Columbus, ATTN: DSCC-VAT, Post Office Box 3990, Columbus, Ohio, 43216-5000 by using the Standardization Improvement Proposal (DD Form 1426) appearing at the end of this document or by letter.

“

### PAGE 3

- \* 2.2.1, STANDARDS, parenthetical source statement, delete “Standardization Document Order Desk” and substitute: “Document Automation and Production Service, DPM-DODSSP”.

### PAGE 4

3.6, resistance tolerance, delete “G ( $\pm 0.5$  percent)” and substitute “D ( $\pm 0.5$  percent)”.

### PAGE 7

- \* After 6.2b, add

“c. Packaging requirements (e.g., Electrostatic discharge (ESD) sensitivity) (see 5.1)”.

- \* 6.3, delete and substitute:

“6.3 Electrostatic charge. Under several combinations of conditions, these resistors can be electrically damaged by electrostatic charges, and drift from specified value. Users should consider this phenomena when ordering or shipping resistors. Direct shipment to the Government is controlled by MIL-DTL-39032 which specifies a preventive packaging procedure.”

MIL-PRF-83401/8D  
AMENDMENT 2

\* After 6.3, add

“6.4 Changes from previous issue. Marginal notations are not used in this revision to identify changes with respect to the previous issue due to the extent of the changes.”

Custodians:

Army - CR  
Navy - EC  
Air Force - 11  
DLA - CC

Preparing activity:

DLA - CC

Review activities:

Army - AR, AT, AV, CR4, MI  
Navy - AS, CG, MC, OS  
Air Force - 19

(Project 5905-1599-08)